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In re the application of	)	
Van Dyke et al.	)	Group Art Unit: 2186
Application No.: 10/657,957	)	Examiner: Choi, Woo H.
Filed: 09/08/03	)	Docket No.
For: SYSTEM AND METHOD FOR A	)	NVIDP033A_P000873
HIGH BANDWIDTH-LOW LATENCY	)	
<u>MEMORY CONTROLLER</u>	)	Date: September 9, 2004

CERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being facsimile transmitted to the Commissioner for Patents, Alexandria, VA 22313-1450 attn: Examiner Choi at facsimile number: (703) 872-9306 on the above date.

Signed: \_\_\_\_\_

Erica L. Farlow

AMENDMENT A

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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